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THIRD EDITION Computer Organization and Design THE HARDWARE/SOFTWARE INTERFACE David A. Patterson University of California, Berkeley John L. Hennessy Stanford University With a contribution by Peter J. Ashenden James R. Larus Daniel J. Sorin Ashenden Designs Pty Ltd Microsoft Research Duke University AMSTERDAM • BOSTON • HEIDELBERG • LONDON

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Courtesy of Computer Organization and Design, 4th edition by David A. Patterson and John L. Hennessy 17 Fig. 2.2.2d Control signal and respective operation 5. Write Back There are two different control signals; MementoReg which is responsible in deciding in between sending the memory value or ALU result from stage 3 and RegWrite which is ...